

#10M
PATENT
81790.0020

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

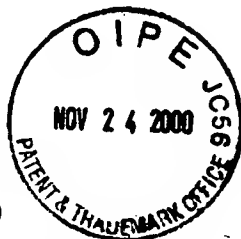
In re application of:

Tomoharu Tanaka

Serial No: 09/456,340

Filed: December 8, 1999

For: FLASH MEMORY CONTROL
METHOD, FLASH MEMORY
SYSTEM USING THE CONTROL
METHOD AND FLASH
MEMORY DEVICE USING THE
CONTROL METHOD



Issue Batch No.: R09

Date of NOA: August 23, 2000

Art Unit: 2818

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Assistant Commissioner for Patents
Washington D.C. 20231, on

November 20, 2000

Date of Deposit

William H. Wright

Name

Signature

11/20/00
Date

TRANSMITTAL OF FORMAL DRAWINGS

Attn: OFFICIAL DRAFTSPERSON
Box ISSUE FEE
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Notice of Allowance dated August 23, 2000, enclosed are the formal drawings (8 sheet(s) – Figure(s) 1-11) in the above-captioned case. All the drawing changes have been made as requested in the Notice of Allowability.

The Commissioner is hereby authorized to charge any deficiency in payment or credit any overpayment to Deposit Account No. 50-1314. A copy of this Transmittal is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By:

William H. Wright

Registration No. 36,312

Attorney for Applicant(s)

Date: November 20, 2000

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